



Revision History

Rev.	Date	Author	Description
0.1	29/4/01	Damjan Lampret	First Draft









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Architecture

Figure 1 below shows general architecture of RTC IP core. It consists of four main building blocks:

- WISHBONE host interface
- RTC registers/circuitry
- Alarm registers/circuitry







Ten hours, with valid values from 0 to 2 when operating in 24 hour mode and 0 to 1 whe

14:11 R/W	0x0	M
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Minutes, with valid values from 0 to 9.

26:23R/W 0x0 TC





