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Connecting to a PC

Storing Non-Volatile Designs in Your XSV Board

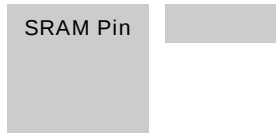
4

XSV Circuitry

This section describes the various sections of the XSV Board and shows how the I/O of the FPGA and CPLD are connected to the rest of the circuitry. The schematics which follow are less detailed so as to simplify the descriptions. Please refer to the complete schematics at the end of this document if you need more details.



Flash RAM



SAA7113
Pin

Virtex

256-entry colormap of the RAMDAC chip. The transfers over the P bus are synchronized

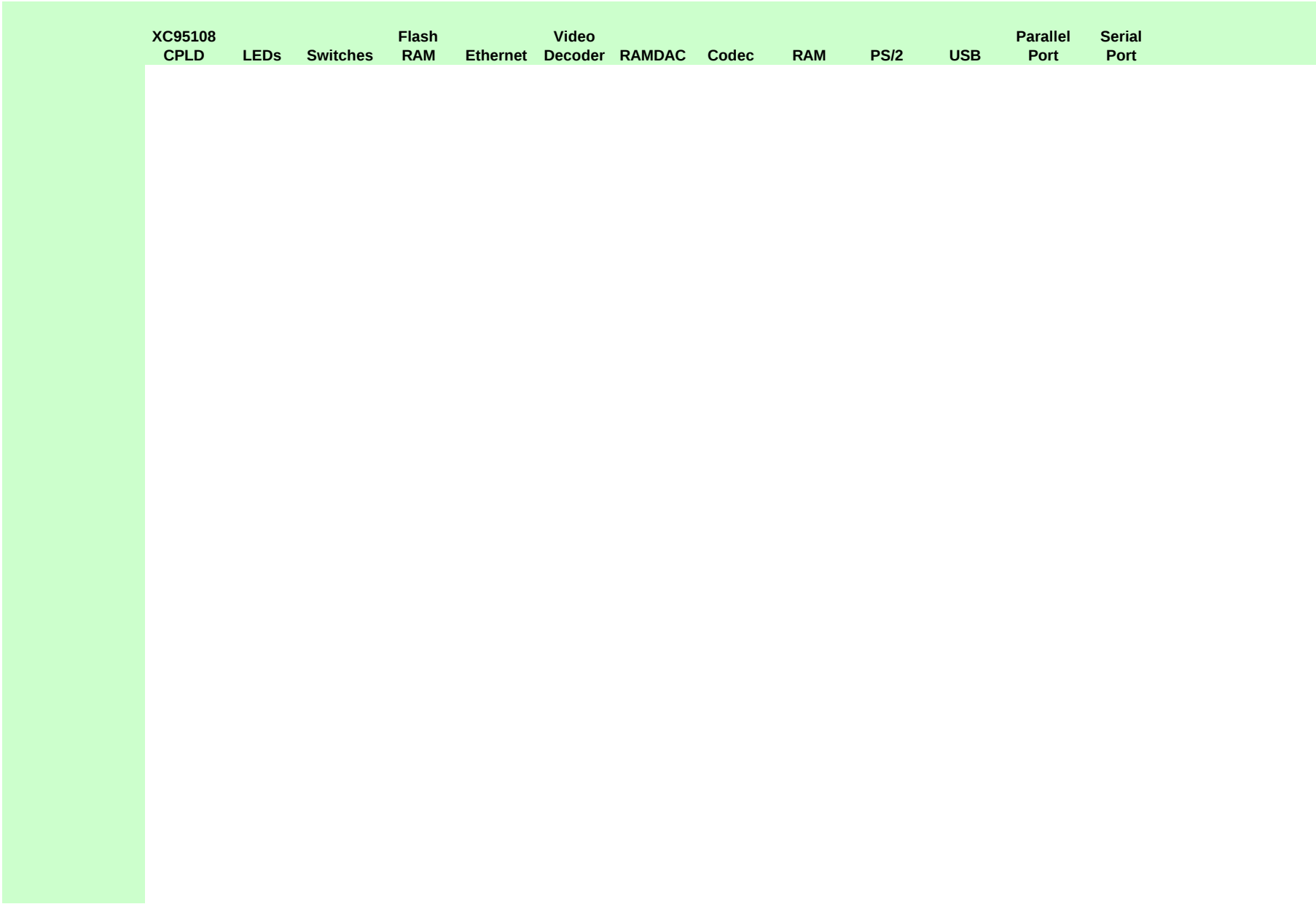
Direct
VGA Pin

RAMDAC

Expansion

LED

Virtex



Connections Between the Virtex FPGA and the Other XSV Board Components															
Virtex FPGA	XC95108 CPLD	LEDs	Switches	Flash RAM	Ethernet	Video Decoder	RAMDAC	Codec	RAM	PS/2	USB	Parallel Port	Serial Port	Prog. Osc.	Xchecker
46							/WR								
47							/RD								
48							/HSYNC								
49							/VSYNC								
50							/BLANK								
51	GND														
52							PIXELCLK								
53									A9 (right)						

Connections Between the Virtex FPGA and the Other XSV Board Components

[illegible]

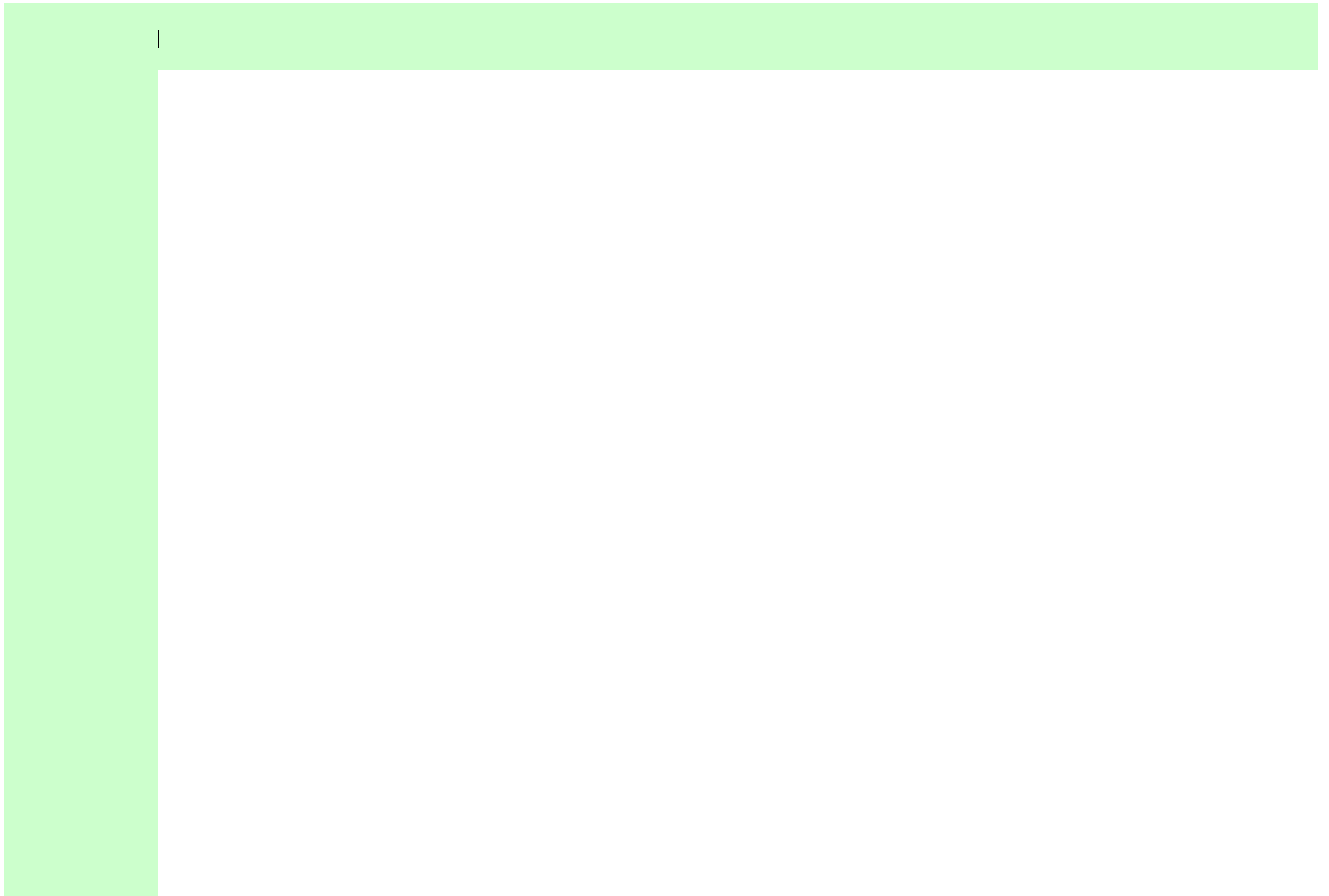
Connections Between the Virtex FPGA and the Other XSV Board Components															
	XC95108			Flash	Video				Parallel	Serial	Prog.				
Virtex FPGA	CPLD	LEDs	Switches	RAM	Ethernet	Decoder	RAMDAC	Codec	RAM	PS/2	USB	Port	Port	Osc.	Xchecker

[illegible]

Connections Between the Virtex FPGA and the Other XSV Board Components

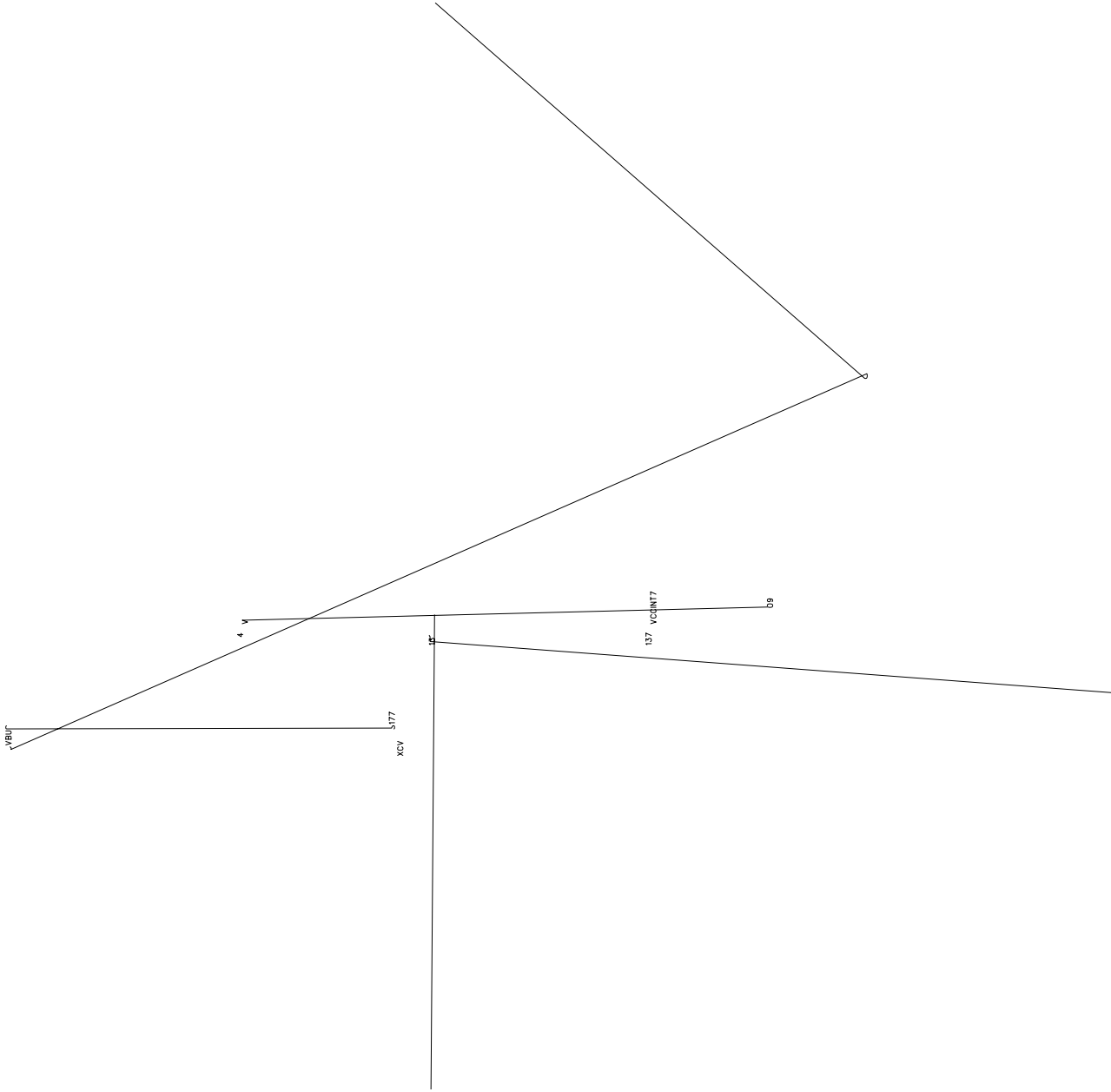
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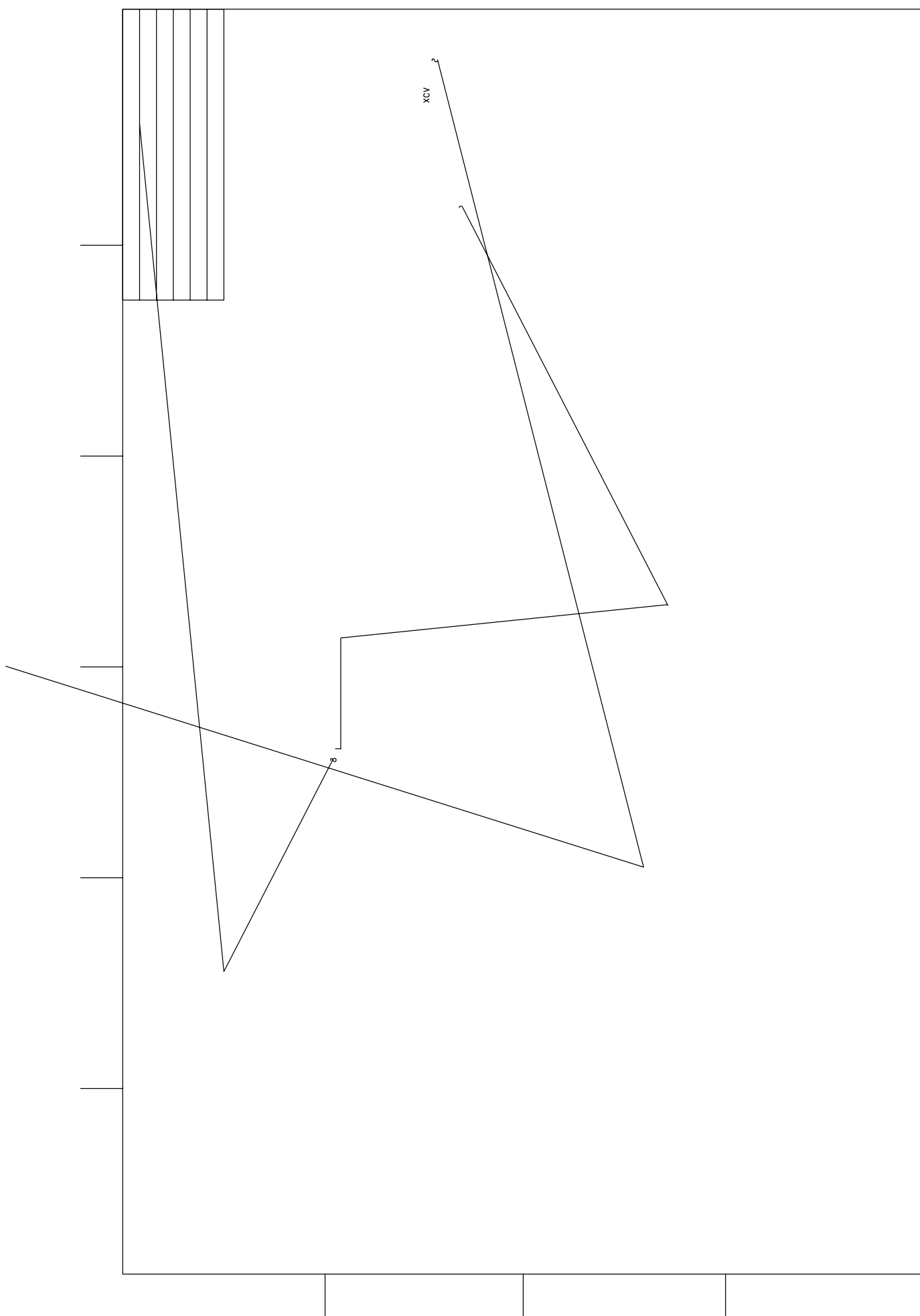
Connections Between the Virtex FPGA and the Other XSV Board Components																
Virtex FPGA		XC95108	Flash		Video		RAM		PS/2		USB		Parallel	Serial	Prog.	Xchecker
		CPLD	LEDs	Switches	RAM	Ethernet	Decoder	RAMDAC	Codec	RAM	PS/2	USB	Port	Port	Osc.	
226	VCCO															
227	GND															
228										/OE (left)						
229										A18 (left)						
		4								A17 (left)						
										A16 (left)						
										A15 (left)						
										A14 (left)						
										A13 (left)						
										A12 (left)						
										A11 (left)						
										A10 (left)						TCK

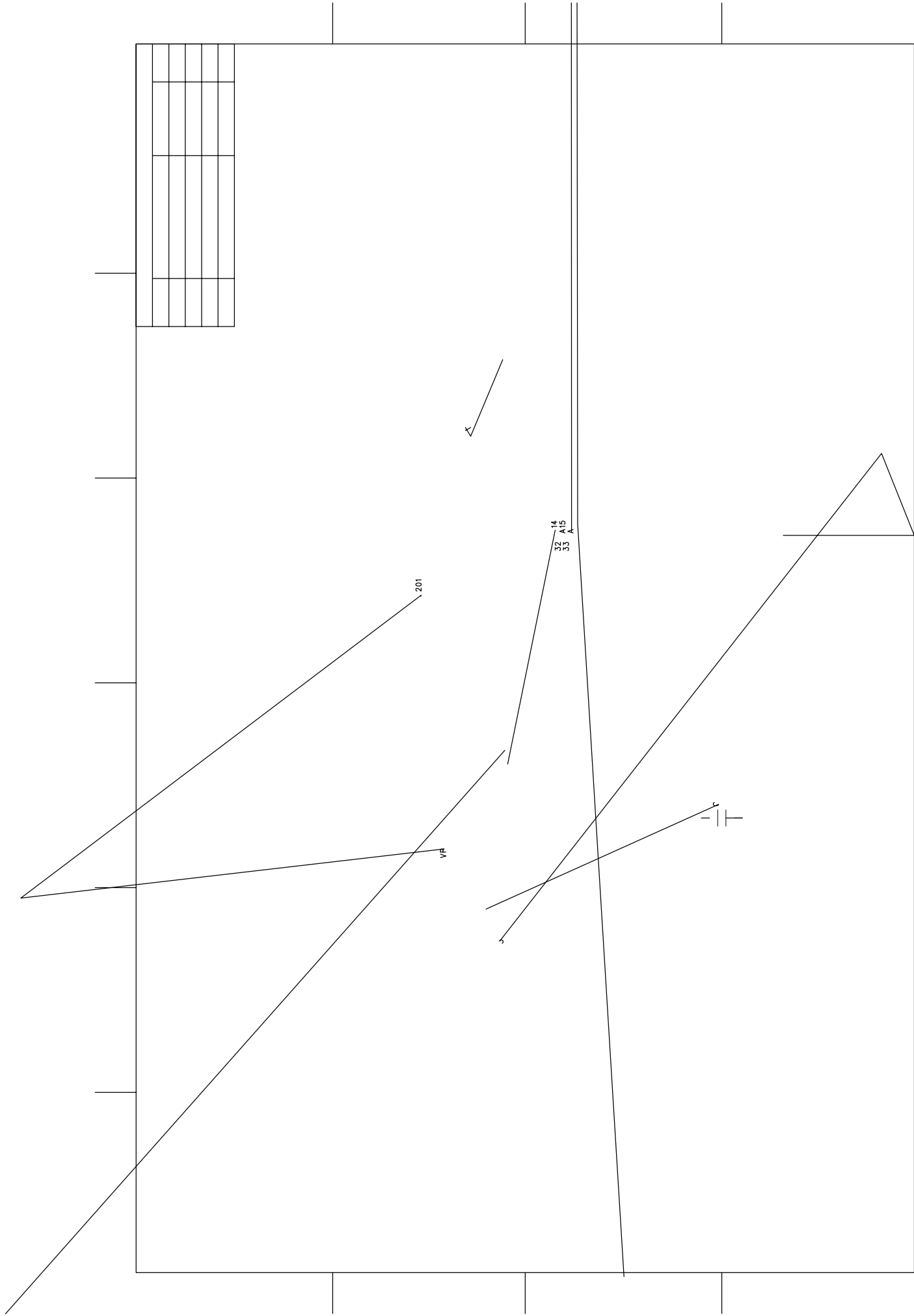


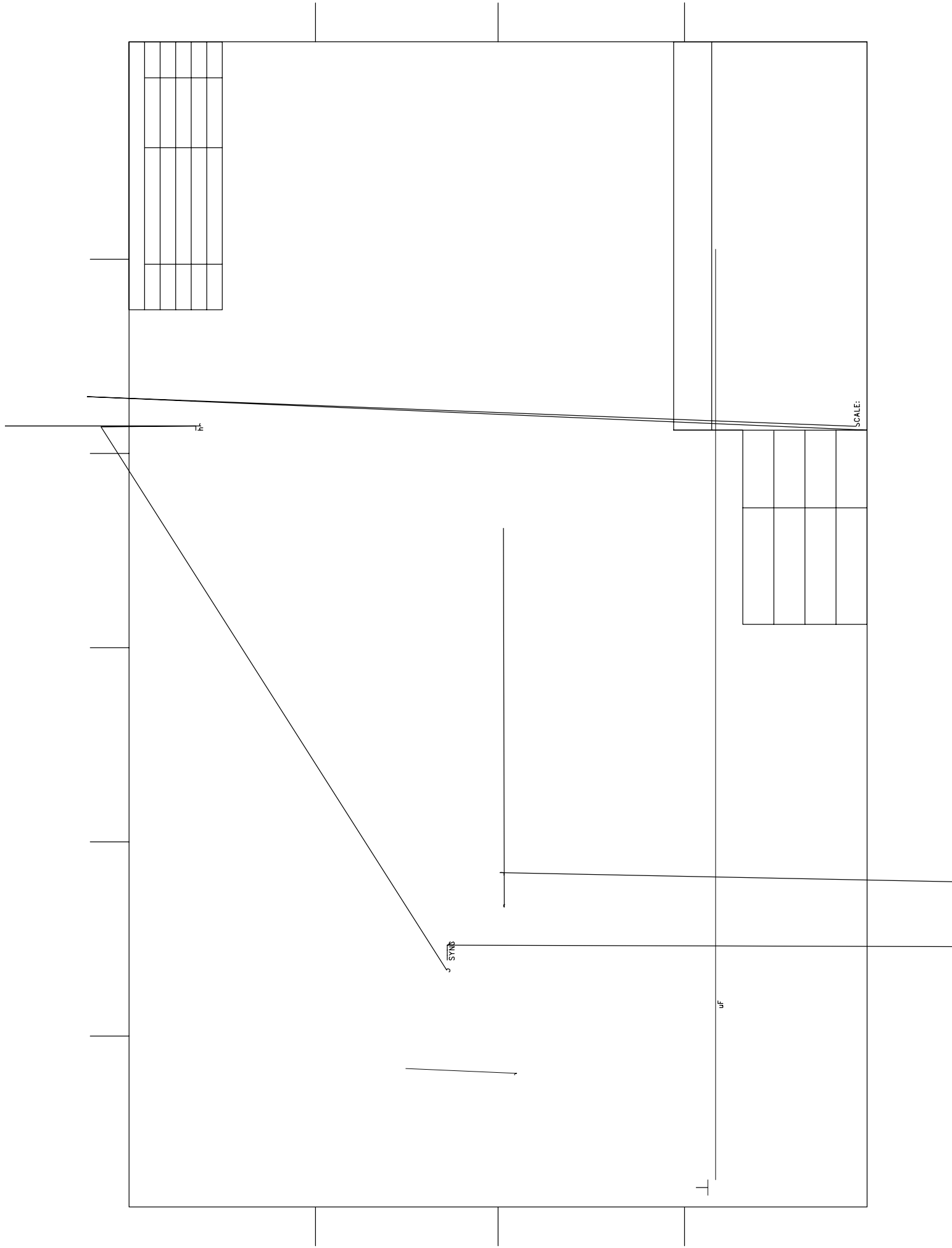
	Virtex FPGA	LEDs	Switches	Flash RAM	Ethernet	Video Decoder	RAMDAC	Codec	RAM	PS/2	USB	Parallel Port	Serial Port	
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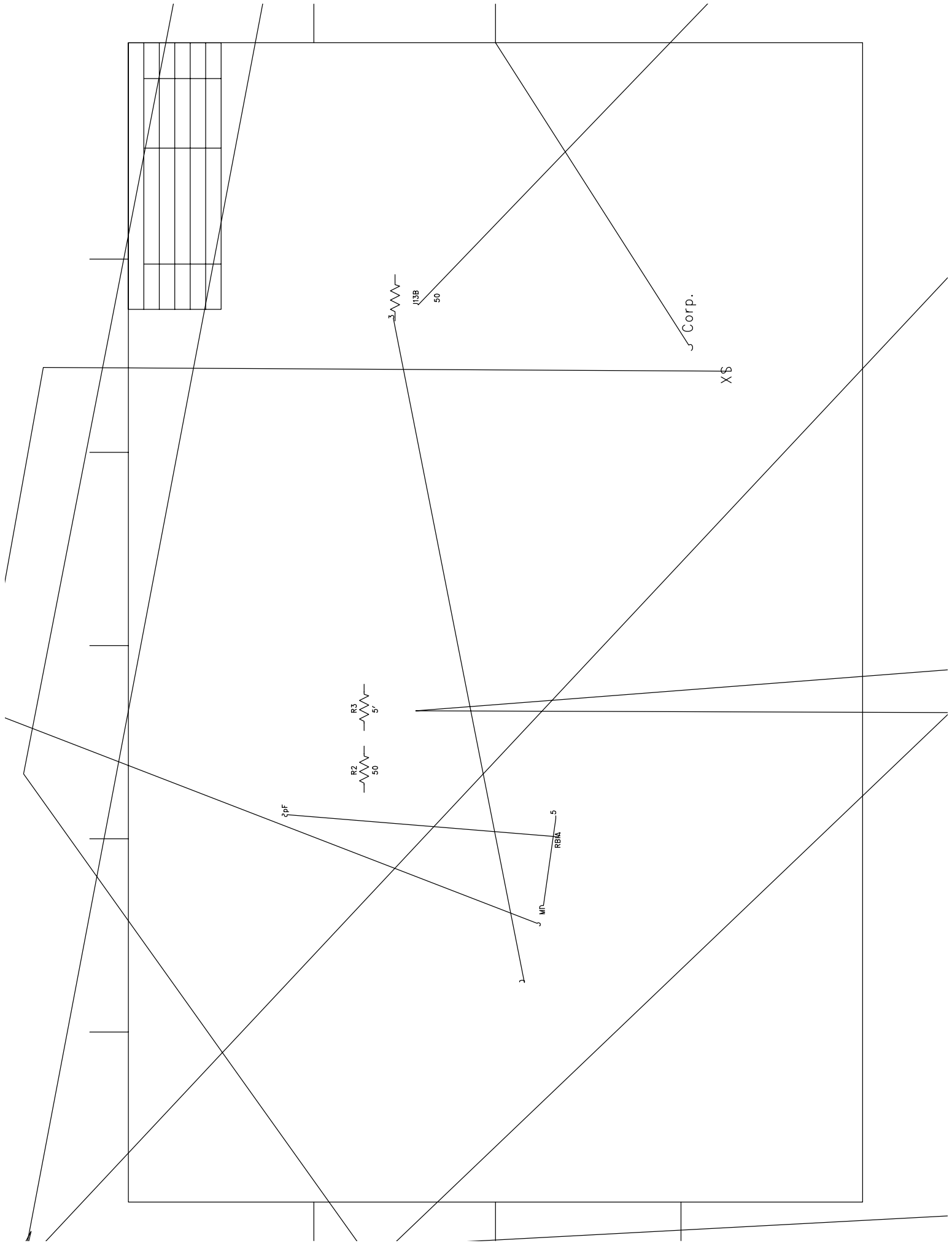
	Virtex FPGA	LEDs	Switches	Flash RAM	Ethernet	Video Decoder	RAMDAC	Codec	RAM	PS/2	USB	Parallel Port	Serial Port	
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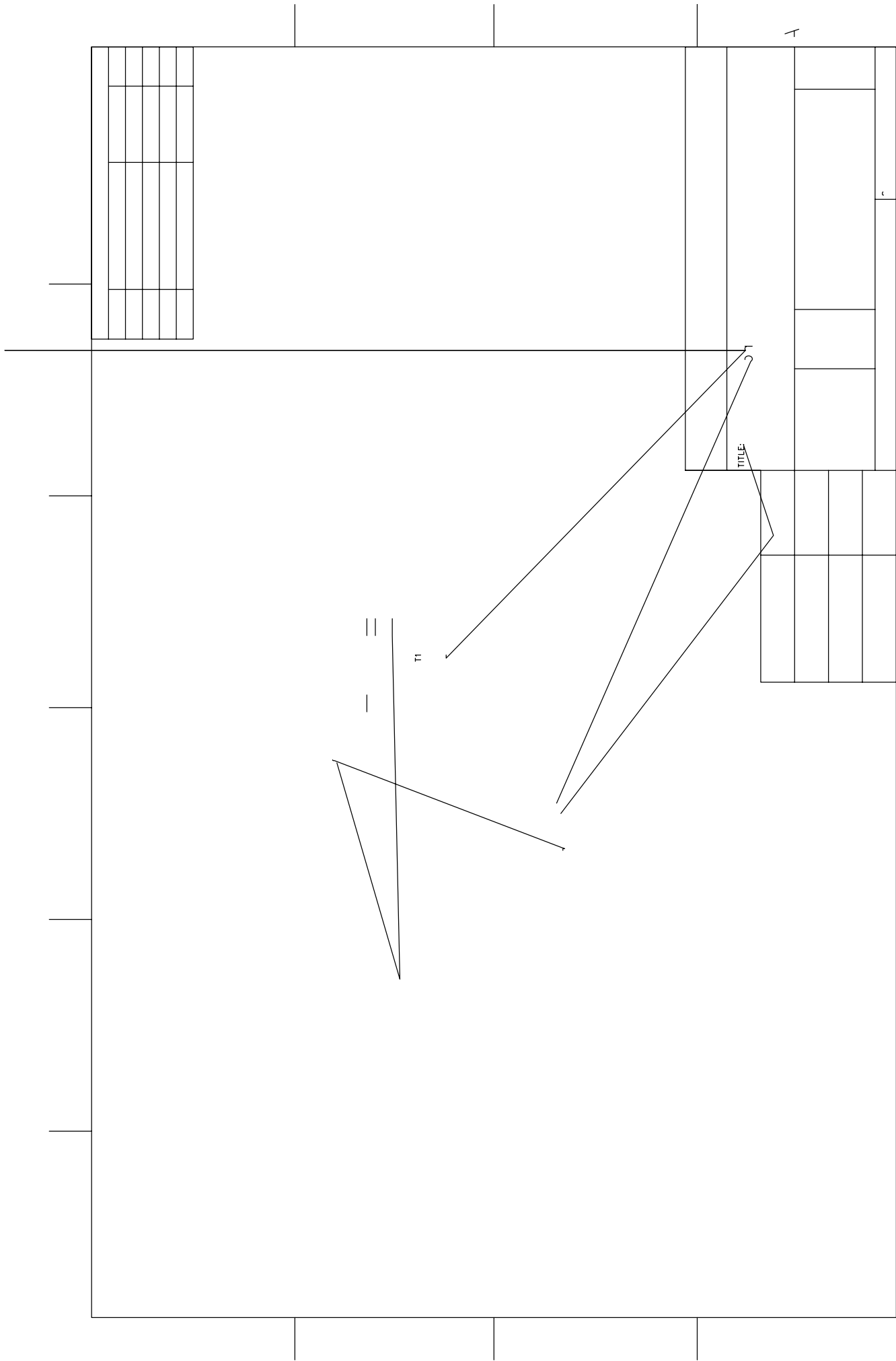


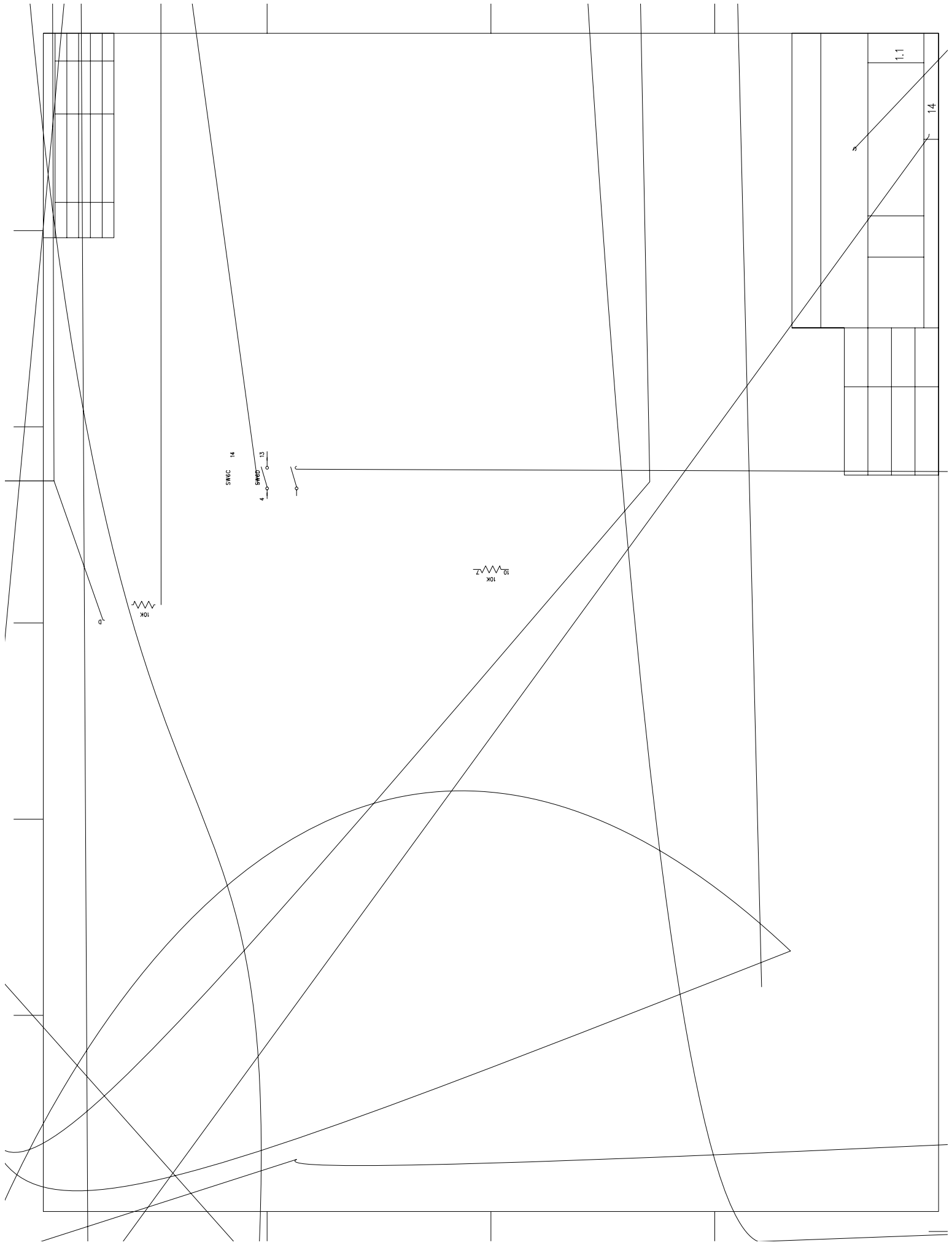












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