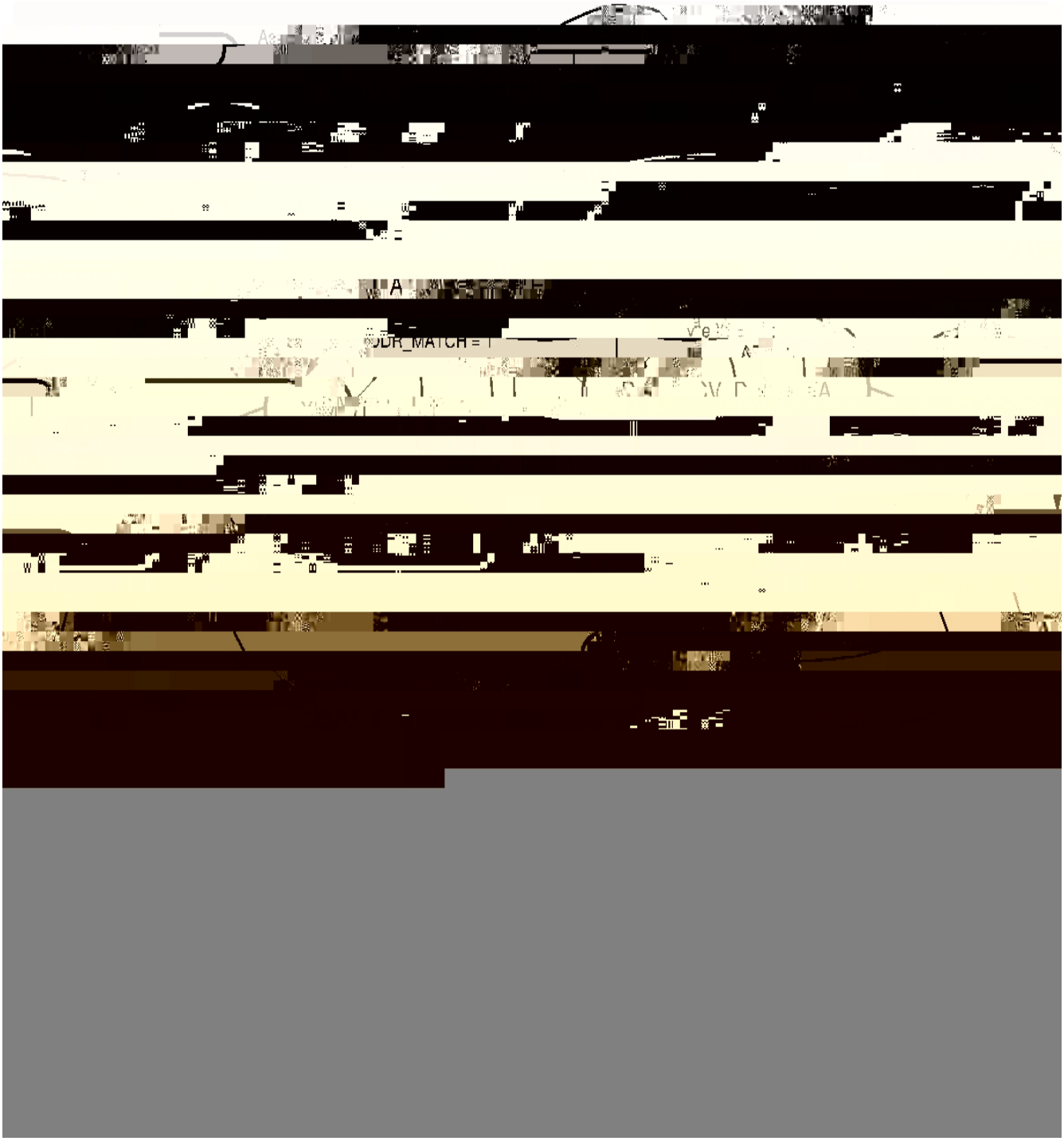
#in Interfaces

#rocessor interf

Registers I ! ple ! ente "

The time%out register is used to determine the m#(imum time th#t SC is #llo$ed to 'e / 0 'efore the "²C st#te m#chine is reset. 0hen the "²C interf#ce is ontf#cefoce"2Tye

State Machine