OpenCores

3.2.1. Recommendation: Make asynchronous active high reset for all flip-flops. Such reset must be synchronized with the clock on SOC top level and it does not require a multiplexer at every flip-flop data input.

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----  This file is part of the XXX project                          ----
----  http://www.opencores.org/cores/xxx/                           ----
----                                                                ----
----  Description                                                   ----
----  Implementation of XXX IP core according to                    ----
----  XXX IP core specification document.                           ----
----                                                                ----
----  To Do:                                                        ----
----   -                                                            ----
----                                                                ----
----  Author(s):                                                    ----
----      - First & Last Name, email@opencores.org                  ----
----                                                                ----
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-- CVS Revision History
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